

**IN THE CLAIMS:**

Please cancel claims 7, 9, 10, 16 and 17 without prejudice or disclaimer, and amend claims 1 and 11-15 as follows:

1. (Currently Amended)

A display device comprising:

a display element;

plural driving circuits;

a display control device which transmits display data and a clock signal to the plural driving circuits; and

a circuit board which is provided between the display control device and the plural driving circuits and supplies the display data and the clock signal transmitted from the display control device, to each of the driving circuits via a bus line and a clock line in the circuit board,

at least one of the bus line and the clock signal line of the circuit board being formed in a continuous area along a long side direction of the circuit board and being divided into plural lines along the long side direction, wherein

the plural driving circuits are arranged at one side of the circuit board,

a connector is arranged at counter side of the circuit board arranged at center of the printed circuit board,

a plurality of connecting lines are connected to at least one of the bus line and the clock signal line and arranged in perpendicular direction to the at least one of the bus line and the clock signal line, and

said divided plural lines are electrically connected to the display control device individually through the connecting lines and connector.

2. (Previously presented) A display device according to claim 1, wherein the display control device supplies the display data and the clock signal to each of the divided bus lines and clock signal lines in sequence in accordance with transmission timing.

3. (Previously presented) A display device according to claim 2, wherein the display control device supplies a signal of fixed voltage level to each of the divided bus lines and clock signal lines to which the display data and the clock signal are not supplied.

4. (Previously presented) A display device according to claim 1, wherein each of the bus line and the clock signal line of the circuit board is divided into two lines.
5. (Previously presented) A display device according to claim 4, wherein the display control device sequentially supplies the display data and the clock signal to one of the bus lines and one of the clock signal lines and to the other of the bus lines and the other of the clock signal lines in sequence in accordance with transmission timing.
6. (Previously presented) A display device according to claim 5, wherein while the display control device is supplying the display data and the clock signal to one of the bus lines and one of the clock signal lines, the display control device supplies signals of fixed voltage level to the other of the bus lines and the other of the clock signal lines.
7. (Canceled).
8. (Previously presented) A display device according to claim 1, wherein the clock signal is a clock signal for latching display data.
9. (Canceled).
10. (Canceled).
11. (Currently Amended.) A display device, ~~comprising~~ according to claim 1, wherein:
  - ~~a display element;~~
  - ~~plural driving circuits;~~
  - ~~a display control device which transmits display data and a clock signal to the plural driving circuits; and~~
  - ~~a circuit board which is provided between the display control device and the plural driving circuits and supplies the display data and the clock signal transmitted from the display control device, to each of the driving circuits via a bus line and a clock line the circuit board;~~

~~at least one of the bus line and the clock signal line of the circuit board being formed in a continuous area along a long side direction of the circuit board and being divided into plural lines along the long side direction.~~

the display control device supplies the display data or the clock signal to at least one of the divided bus lines or clock signal lines.

the display control device supplies a signal of fixed voltage level to the at least one of the divided bus lines or clock signal lines to which the display data or the clock signal is not supplied.

12. (Currently Amended.) A display device ~~comprising~~ according to claim 1, wherein:

~~a display element;~~

~~plural driving circuits;~~

~~a display control device which transmits display data and a clock signal to the plural driving circuits; and~~

~~a circuit board which is provided between the display control device and the plural driving circuits and supplies the display data and the clock signal transmitted from the display control device, to each of the driving circuits via a bus line and a clock line the circuit board,~~

~~the bus line of the circuit board being formed in a continuous area along a long side direction of the circuit board and being divided into plural lines along the long side direction;~~

the display control device supplies the display data and the clock signal to the divided bus lines or clock signal,

the display control device supplies a signal of fixed voltage level to either one of the divided bus.

13. (Currently Amended.) A display device ~~comprising~~ according to claim 1, wherein:

~~a display element;~~

~~plural driving circuits;~~

~~a display control device which transmits display data and a clock signal to the plural driving circuits; and~~

~~a circuit board which is provided between the display control device and the plural driving circuits and supplies the display data and the clock signal transmitted~~

~~from the display control device, to each of the driving circuits via a bus line and a clock line the circuit board,~~

~~at least the bus line of the circuit board being formed in a continuous area along a long side direction of the circuit board and being is divided into two lines along the long side direction,~~

~~the display control device sequentially supplies the display data to one of the bus lines and to the other of the bus lines in sequence in accordance with transmission timing,~~

~~while the display control device is supplying the display data to one of the bus bores, the display control device supplies signals of fixed voltage level to the other of the bus lines.~~

14. (Currently Amended) A display device ~~comprising~~ according to claim 1, wherein:

~~a display element;~~

~~plural driving circuits;~~

~~a display control device which transmits display data and a clock signal to the plural driving circuits; and~~

~~a circuit board which is provided between the display control device and the plural driving circuits and supplies the display data and the clock signal transmitted from the display control device, to each of the driving circuits via a bus line and a clock line the circuit board,~~

~~at least one of the bus line of the circuit board formed in a continuous area along a long side direction of the circuit board and being is divided into two lines along the long side direction.~~

~~the display control device sequentially supplies the display data to one of the bus lines and to the other of the bus lines in sequence in accordance with transmission timing,~~

~~while the display control device is supplying the clock signal to one of the clock signal lines, the display control device supplies signals of fixed voltage level to the other of the clock signal lines.~~

15. (Currently Amended.) A display device ~~comprising~~ according to claim 1, wherein:

~~a display element;~~

~~plural driving circuits;~~

~~a display control device which transmits display data and a clock signal to the plural driving circuits; and~~

~~a circuit board which is provided between the display control device and the plural driving circuits and supplies the display data and the clock signal transmitted from the display control device, to each of the driving circuits via a bus line and a clock line the circuit board,~~

~~the bus line and the clock signal line of the circuit board being formed in a continuous area along a long side direction of the circuit board and being is divided into two lines along the long side direction;~~

the display control device sequentially supplies the display data and the clock signal to one of the bus lines and one of the clock signal lines and to the other of the bus lines and the other of the clock signal lines in sequence in accordance with transmission timing,

while the display control device is supplying the display data and the clock signal to one of the bus lines and one of the clock signal lines; the display control device supplies signals of fixed voltage level to the other of the bus lines and the other of the clock signal lines.

16. (Canceled).

17. (Canceled).